

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kenneth Graham Paterson)	Re: Information Disclosure
)	Statement
Serial No.: not yet assigned)	
Filed: concurrently herewith)	Group: 2672
)	
Divisional of USSN <u>09/381,010</u>)	Examiner: Daniel J. Chung
filed on <u>September 13, 1999</u>)	
)	
For: "DECODER SYSTEM CAPABLE OF)	Our Ref: B-3719DIVofPCT
PERFORMING A PLURAL-STAGE)	621147-1
PROCESS")	
)	Date: September 23, 2003

MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the Applicant's duty to disclose information which may be material to the examination of this application, the undersigned respectfully requests that the documents cited by the Examiner and/or submitted by the Applicant in connection with U.S. Serial No. 09/381,010 filed on September 13, 1999 (the parent of the Applicants' subject application) be considered by the Examiner before issuing the first Office Action on the merits.

For the Examiner's convenience, Form PTO-1449 (modified) is enclosed herewith listing the documents cited by the Examiner and/or submitted by the Applicant in connection with U.S. Serial No. 09/381,010.

The filing of this Information Disclosure Statement (IDS) shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

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If any fees are due, please charge our Deposit Account No. 12-0415 or credit any overpayment to our Account No. 12-0415. No fees should be due because this Information Disclosure Statement is being filed concurrently with the above-identified U.S. patent application.

The filing of this Information Disclosure Statement shall not be construed as an admission against interest in any manner. (Notice of January 9, 1992, 1135 O.G. 13-25, at 25.)

The person making this statement is the practitioner who signs below on the basis of information supplied by an individual associated with the filing and prosecution of this application (37 C.F.R. § 1.56(c)) and on the basis of information in the practitioner's file.

Respectfully submitted,



Richard P. Berg
Attorney for Applicant
Reg. No. 28,145

LADAS & PARRY
5670 Wilshire Boulevard
Suite 2100
Los Angeles, CA 90036
Telephone: (323) 934-2300
Telefax: (323) 934-0202

Enclosures: Form PTO-1449 (modified) (2 pages)

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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-3719DIVofPCT 621147-1	U.S. SERIAL NO. not yet assigned
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT(S) Kenneth Graham Paterson	
	FILING DATE concurrently herewith	GROUP 2672

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
	3,871,003	3/1975	Kondo et al.	346	74	
	3,898,616	8/1975	Baugh et al.	340	146	
	4,104,735	8/1978	Hofmann et al.	365	230	
	5,021,688	6/1991	Leforestier et al.	307	463	
	5,034,736	7/1991	Bennett et al.			
	5,122,873	6/1992	Golin	358	133	
	5,278,961	1/1994	Mueller			
	5,369,621	11/1994	Mason	365	230.06	
	5,412,614	5/1995	Bird	365	230.06	
	5,430,461	7/1995	Tsai			
	5,592,160	1/1997	Bennett et al.	341	58	
	5,640,365	6/1997	Imamiya et al.	365	236	
	5,721,709	2/1998	Nakamura			
	5,852,469	12/1998	Magai et al.	348	384	
	6,243,287 B1	6/2001	Naffziger et al.	365	154	
	6,275,442 B1	8/2001	Hill et al.	365	230.06	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
JP 8-287686	11/1996	JP			

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Beth, T., <i>Design Theory</i> , Cambridge University Press, 1993. pp. 305-346.
	Beth, T., <i>Design Theory</i> , Vol. 2, Cambridge University Press, 1999, pp. 937-939.
	Brouwer, A.E., et al., "A New Table of Constant Weight Codes", <i>IEEE Transactions on Information Theory</i> , Vol. 36, No. 6, November 1990, pp. 1334-1380.
	Kobayashi, M., et al., "Electrostatic Recording Method Using Transformer Assemblies Based on Error-Correcting Codes", <i>IEEE Transactions on Electron Devices</i> , Vol. Ed-25, No. 1, January 1978, pp. 1-7.
	MacWilliams, F.J., et al., <i>Theory of Error-Correcting Codes</i> , 1977, pp. 307-315, 523-531, 683-691.
	Minnick, R.C., et al., "Magnetic Core Access Switches", <i>IRE Transactions on Electronic Computers</i> , June 1962, pp. 352-368.
	Moreno, O., et al., "New Constructions of Optimal Cyclically Permutable Constant Weight Codes", <i>IEEE Transactions on Information Theory</i> , Vol. 41, No. 2, March 1995, pp. 448-455.
	Nguyen, Q.A., et al., "Constructions of Binary Constant-Weight Cyclic Codes and Cyclically Permutable Codes", <i>IEEE Transactions on Information Theory</i> , Vol. 38, No. 3, May 1992, pp. 940-949.
	Singleton, R., "Load-Sharing Core Switches Based on Block Designs", <i>IRE Transactions on Electronics Computers</i> , June 1962, pp. 346-352.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.